Fast HW/SW Co-Verification Solution Using HAPS Prototype
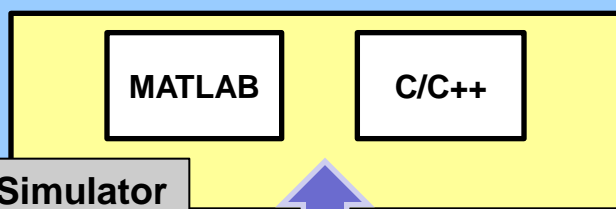
◆ Fast HW/SW co-verification
◆ User-friendly verification platform

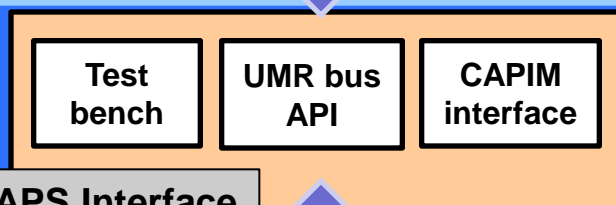**RADRIX**
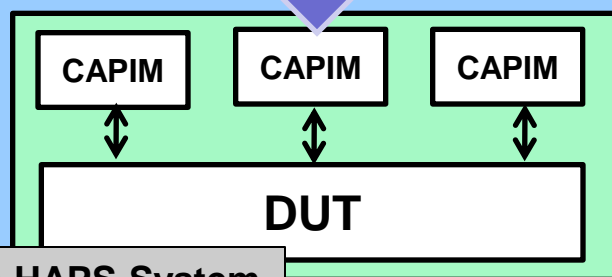*for Future Mobile Technologies*

MATLAB | **C/C++**

System Simulator

Test bench | **UMR bus API** | **CAPIM interface**

Radrix HAPS Interface

CAPIM | **CAPIM** | **CAPIM**

DUT

Synopsys HAPS System

Radrix HW/SW Co-Verification Platform

Radrix solution supports:

✓ Fast HW/SW Co-Verification

✓ Scalable design

✓ Real-time simulation

✓ VLSI design

Synopsys HAPS system supports:

✓ Wireless communication application, processor design, etc.

✓ VLSI designs (more than 10 millions ASIC gates).

✓ High-performance Universal Multi-Resources Bus (UMR Bus).

RADRIX
for Future Mobile Technologies

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